

OFFICE OF THE HEAD
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

No. EIE/111

Date: 08.05.2017

NOTICE

The following candidates have been shortlisted (Provisionally) for written test and interview before the selection committee for admission into PhD program in the Department of E&I Engineering, NIT Silchar for the session July-December, 2017.

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Date of reporting: 29th May, 2017

Time : 9:30 a.m. to 10 a.m.

Venue : Office of the HOD, Department of E&I Engineering.

N.B.

- i. Employed persons must produce a sponsorship certificate/NOC, if a copy of the same has not been attached earlier along with the application.
- ii. You are to produce all original mark sheets, certificates/testimonials/valid GATE score card for verification at the time of interview.
- iii. No TA/DA shall be admissible to the candidates for appearing for the interview.

Note: For any queries please contact HOD, Electronics & Instrumentation Engineering, NIT Silchar, Assam.

Sd/-

Head,
Electronics & Instrumentation Engineering,
NIT Silchar