

OFFICE OF THE HEAD  
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

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No. EIE/111/18

Date: 31.05.2018

**Shortlisted candidates for PhD Admission**

The following candidates have been shortlisted (Provisionally) for written test and interview before the selection committee for admission into PhD program in the Department of E&I Engineering, NIT Silchar for the session July-December, 2018.

Sl. No.	Name	Email-ID
1	Krishanu Nath	krishanunathee2015@gmail.com
2	Natha Dhnunjayarao	dhanunjayaraonatha@gmail.com
3	Khoirom Johnson Singh	khoirm@rediffmail.com
4	Rajrup Saha	rajrupslg@gmail.com
5	Md Sarful Alam	sarfulalam5@gmail.com
6	Jusmita Das	jusmita.nita2008@gmail.com
7	Pratik Goswami	pratigoswami91@gmail.com
8	Abul Abbas Barbhuiya	abulabbasnet@gmail.com
9	Mayank Kumar Gautam	prince240791@gmail.com
10	Sujata S. Kulkarni	adwitideoghare@gmail.com
11	Trigunesh Narzary *	triguneshnarzary@gmail.com
12	Vikas Kushwah	vikaskushwah25@gmail.com
13	Ashish Ranjan	ashish150289@gmail.com
14	Debika Jamatia **	debikajamatia1993@gmail.com
15	Harun Al Rashid MD Ghalib	hrmdghalib@gmail.com
16	Pragnaleena Debroy	pragnaleenadebroy93@gmail.com
17	Ramashis Banerjee **	ramashisbanarjee@gmail.com
18	Shyamosree Goswami *	shyamosreegoswami@gmail.com
19	Angaraj Das	angaraj.dass@gmail.com
20	Rajarshi Das	sumonds4@gmail.com
21	Shashi Kant Chaudhary	shashi.rgcc@yahoo.com
22	Athena Chakraborty	nit.athena@gmail.com
23	Athena Chakraborty	nit.athena@gmail.com
24	Uttara Biswas **	biswasuttara@gmail.com

\* Provisionally shortlisted. The candidate has to submit their original DD on the day of reporting.

\*\* Provisionally shortlisted. A fair hard copy of application form & DD along with copy of required documents are to be submitted on the day of reporting.

Date & time of written test & interview:

**Date of reporting: 8<sup>th</sup> June, 2018**

Time : 9:00 a.m. to 10 a.m.

Venue : Office of the HOD, Department of E&I Engineering.

**N.B.**

- i. Employed persons must produce a sponsorship certificate/NOC, if a copy of the same has not been attached earlier along with the application.
- ii. You are to produce all original mark sheets, certificates/testimonials/valid GATE score card for verification at the time of interview.
- iii. No TA/DA shall be admissible to the candidates for appearing for the interview.

*Note: For any queries please contact HOD, Electronics & Instrumentation Engineering, NIT Silchar, Assam.*

Sd/-

Head,  
Dept. of Electronics & Instrumentation Engineering,  
NIT Silchar