

Department of Electronics and Communication Engineering

National Institute of Technology Silchar

Programme Educational Objectives (PEOs)

The objectives of the M. Tech in Microelectronics and VLSI Design programme of National Institute of Technology Silchar are-

- PEO-1:** The Graduates of VLSI Design will demonstrate their outstanding education skills that will enable them to integrate undergraduate fundamentals with the knowledge acquired to evaluate and analyze new developments in VLSI industry
- PEO-2:** The Graduates of VLSI Design will demonstrate advancement in engineering to engage in perpetual learning in order to suit multidisciplinary situations.
- PEO-3:** The Graduates of VLSI Design would undertake a significant research or development projects.
- PEO-4:** The VLSI Design Graduates would demonstrate his/her analytical skills to solve real time problems.
- PEO-5:** The VLSI Design Graduates will demonstrate their professional, ethical and social issues and show respect for diversity and global issues.