Department of Electronics and Communication Engineering

National Institute of Technology Silchar

Programme Outcomes (POs) of M. Tech in Microelectronics and VLSI Design are as follows-

PO-1: Students will be able to apply knowledge from undergraduate engineering and other disciplines to identify, formulate and present solutions to technical problems in various engineering fields related to VLSI design and technology.

PO-2: Students will be able to learn advanced technologies in the fields of VLSI design along with the fundamental concepts.

PO-3: Students will be able to apply advanced technical knowledge in multiple contexts.

PO-4: Students will be able to understand and design advanced VLSI based system and conduct experiments, analyze and interpret results.

PO-5: Students will be able to use the techniques, skills, modern Electronic Design Automation (EDA) tools, software and equipment necessary to evaluate and analyze the systems in VLSI design environments.

PO-6: Students will be able to plan, conduct an organized and systematic study on significant research topic within the field of VLSI and its allied field.

PO-7: Students will be able to convey technical material through formal written reports which satisfy accepted standards of writing style.

PO-8: Students will be able to communicate professionally.

PO-9: Students will be able to become knowledgeable about contemporary developments.

PO-10: Students will be able to develop confidence for self-education and lifelong learning.

PO-11: Students will be able to participate and succeed in competitive examinations.