

NATIONAL INSTITUTE OF TECHNOLOGY SILCHAR

1ST SEM M. TECH ELECTRONICS & COMMUNICATION ENGG. TABULATION SHEET, NOVEMBER 2019 (Provisional)

Microelectronics & VLSI Design.

Sl. No.	Registration No.	EC 2501		EC 2502		EC 2503		EC 2531		EC 2542/EC 2544		EC 2504		EC 2510		TCP	TGP/18	SGPA 1st sem	SGPA/ CGPA Below 6.0
		Semiconductor Device Physics		Digital VLSI Design		Device Modelling		Design & Synthesis using Verilog HDL (EI-I)		Foundation of VLSI CAD/Low Power VLSI (EI- II)		VLSI Lab-1		Seminar					
		Credit	3	Credit	3	Credit	3	Credit	3	Credit	3	Credit	2	Credit	1				
1	1924201	BB	8	BC	7	BC	7	BB	8	BB	8	AB	9	CD	5	18	137	7.61	-
2	1924202	BC	7	CC	6	BC	7	CD	5	AB	9	BB	8	CC	6	18	124	6.89	-
3	1924203	AB	9	AB	9	AB	9	BB	8	AB	9	AB	9	BB	8	18	158	8.78	-
4	1924204	BC	7	BC	7	DD	4	CD	5	BB	8	AB	9	BC	7	18	118	6.56	-
5	1924205	BB	8	BB	8	CD	5	BC	7	BB	8	AB	9	CC	6	18	132	7.33	-
6	1924206	BC	7	CC	6	CC	6	CD	5	BC	7	BB	8	BC	7	18	116	6.44	-
7	1924207	AB	9	CC	6	BC	7	DD	4	BC	7	BB	8	BC	7	18	122	6.78	-
8	1924208	CD	5	CD	5	DD	4	DD	4	CD	5	AB	9	BC	7	18	94	5.22	***
9	1924209	BC	7	CC	6	DD	4	CD	5	BC	7	BC	7	CC	6	18	107	5.94	***
10	1924210	AB	9	BB	8	AB	9	CC	6	BC	7	BB	8	F	0	18	133	7.39	-
11	1924211	CC	6	CC	6	BC	7	CC	6	BC	7	AB	9	BC	7	18	121	6.72	-
12	1924212	AB	9	BB	8	BB	8	BB	8	BC	7	BB	8	BC	7	18	143	7.94	-
13	1924213	AB	9	BB	8	BB	8	BB	8	BB	8	BB	8	BB	8	18	147	8.17	-
14	1924215	AB	9	AB	9	AB	9	BB	8	AB	9	AA	10	BC	7	18	159	8.83	-
15	1924216	AB	9	BC	7	BC	7	BC	7	BB	8	BB	8	BC	7	18	137	7.61	-
16	1924217	BC	7	BC	7	CD	5	CC	6	BB	8	BB	8	CC	6	18	121	6.72	-
17	1924218	F	0	F	0	F	0	F	0	F	0	F	0	F	0	18	0	0.00	***
18	1924219	CC	6	BC	7	CC	6	BC	7	BC	7	BC	7	BC	7	18	120	6.67	-
19	1924220	AB	9	AB	9	BC	7	BB	8	BB	8	AA	10	AB	9	18	152	8.44	-
20	1924221	BB	8	BC	7	BC	7	DD	4	BC	7	BB	8	BC	7	18	122	6.78	-

EI-I : EC 2531 Design and Synthesis using Verilog HDL. **EI-II** : 1. EC 2542 Foundations of VLSI CAD- (Normal). 2. EC 2544 Low Power VLSI- (Red Colour).

BY ORDER